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Weiss, Moy & Harris, PC
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Rabin & Berdo, PC

March 31, 2004

Mail Stop Patent Application

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Attorney Reference: OKI 417

Re: New Patent Application of: Seiichiro SASAKI and Kouji MORITA
Title: MULTILAYERED POWER SUPPLY LINE FOR SEMICONDUCTOR
INTEGRATED CIRCUIT AND LAYOUT METHOD THEREOF

Sir:

Please find attached hereto an application for patent which includes:

- ☒ Specification, Claims and Abstract (24 Pages)
- ☒ 5 Sheets of Formal Drawings (Fig. 1 through Fig. 13)
- ☒ Inventor's Declaration and Power of Attorney (3 Pages)
- ☒ Assignment document with cover page (3 Pages)
- ☒ Claim for priority and the Priority document
- ☒ Information Disclosure Statement, Form PTO-1449, and copies of 2 references
- ☒ Form PTO-2038
- ☒ Fee (see formula below)

Basic Fee \$770/385 \$ 770.00

Additional Fees:

Total number of claims: 15
in excess of 20: 0 times \$18/9 \$ 0.00

Number of independent claims: 3
in excess of 3: 0 times \$86/43 \$ 0.00

Multiple Dependent Claims \$290/145 \$ 0.00

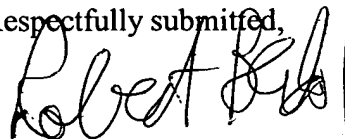
Recording Fee \$40. \$ 40.00

TOTAL FEES FOR THE ABOVE APPLICATION \$ **810.00**

In the event there is attached hereto no credit card form PTO-2038 or check, or there is an insufficient amount, please charge the fee to our Account No. 18-0002 and notify us accordingly.

The rights of priority are claimed under 35 USC §119 of Japanese Application No. 2003-397724, filed November 27, 2003.

Respectfully submitted,



Robert H. Berdo, Jr. (Reg. No. 38,075)
(Customer No. 23995)

RHB:tz



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